



**Rajasthan Institute of Engineering & Technology, Jaipur**  
**Department of Electrical & Electronics Engineering**

**Effective from:- 02/01/2017**

**2nd Year; 4th Sem (A/B); Time-Table (January-2017)**

**LT-6/7**

Period	Batch	I	II	III	IV	V	Lunch	VI	VII
Time /day		08:10-09:05	09:05-10:00	10:00-10:55	10:55-11:50	11:50-12:45	12:45-01:40	01:40-02:35	02:35-03:30
Mon	A	LIC	EM-T	CRT	EM-II	AEM-II	LUNCH	IC Lab	
	B	CA-II	GEP					EM Design Lab	
Tues	A	EEM	AEM-II	AE	EM-II	EM		EM Design Lab	AE Lab
	B	GEP						EM-II Lab	
Wed	A	EM-II	EM	AE	LIC	LIC-T		EM Lab	EM Lab
	B				EM-T	CA-II		EM Lab	EM-II Lab
Thur	A	EEM	AE	EEM-T		AEM-II		EM Lab	EM-II Lab
	B	GEP		EM-II-T			AE Lab		
Fri	A	LIC	EM	EM-II-T	EEM	AE	AE Lab	PSD Lab	
	B	CA-II		AE-T	CA-II-T				

**Mr. Harish Kumar Maheshwari**

Time Table Incharge

**Mr. Hemant Kaushik**

HOD