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**Department of Electronics and Communication Engineering**

**External Examination Schedule**

**II year (III Sem)**

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| **S.No.** | **Date** | **Lab** | **Internal Examiner** | **External Examiner** | **Timing Slot** |
| 1 | 29/11/2017 | Electronic Instrumentation Workshop | Mr. Sandeep | Mr. Snehashis Jha | 9:00-11:00 |
| 2 | 29/11/2017 | Computer Programming Lab-I | Ms. Jyoti Khandelwal  | Mr. Rajesh Jain | 1:00-3:00 |
| 3 | 30/11/2017 | Electronic Device Lab | Mr. Ravikaant | Mr. Shashikant Ray/ Mr. Omprakash Choudhary | 9:00-11:00 |
| 4 | 30/11/2017 | Digital Electronics Lab | Ms. Yashika Saini | Mr. Vivek Khandelwal | 1:00-3:00 |
| 5 | 01/12/2017 | Business Entrepreneurship | Mr. Preeti Nagar | Mrs. Vijaya Chakarwati | 9:00-11:00 |
| 6 | 01/12/2017 | Discipline & Extra Curricular Activity | Mr. Omprakash Choudhary | Mr. Shashikaant Ray | 1:00-3:00 |

HOD