****

**Department of Electrical and Electronics Engineering**

**External Examination Schedule Main/ Back**

**II year (III Sem)**

**EE / EEE**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S.No.** | **Date** | **Lab** | **Internal Examiner** | **External Examiner** | **Timing Slot** |
| 1 | 29/11/2017 | Electronic Device Lab | Mr. Narendra Saini | Mr. Harish Maheshwari | 9:00-11:00 |
| 2 | 29/11/2017 | Computer Programming C++ Lab-I | Mr. Dinesh swami | Mrs. Nirmala Choudhary | 1:00-3:00 |
| 3 | 30/11/2017 | Circuit Analysis Lab | Mr. Amarpreet Singh | Mr. Ashish Jaiswal | 9:00-11:00 |
| 4 | 30/11/2017 | Discipline & Extra Curricular Activity | Mr. Narendra Saini | | 1:00-3:00 |
| 5 | 01/12/2017 | Digital Electronics Lab | Mr. Narendra Saini | Mr. Tushar Agarwal | 9:00-11:00 |
| 6 | 01/12/2017 | HSS Lab | Ms. Garima Verma | Mr.Amarpreet Singh | 1:00-3:00 |

HOD