**RAJASTHAN INSTITUTE OF ENGINEERING & TECHNOLOGYJAIPUR, RAJASTHAN**

**B. Tech. II Midterm EXAMINATION 2017-18**

**Semester and Branch**: II Year, IV Semester, EEE

**Subject:** LIC

**SET B**

***Duration*: 2 Hours             *Maximum Marks*: 20**

**Student Instructions:**

* **All questions are compulsory.**
* **All questions carry equal marks.**
* **Use pencils for the diagrams.**

1. Draw and explain with suitable diagram First-order High pass Butterworth filter.

OR

Draw and explain with suitable diagram First-order ALL pass Butterworth filter.

1. Write short note on “switched capacitor filter”.

OR

Explain the Limitation of Analog Circuit and how we Solve these limitation.

1. Explain the operation of PLL as a Frequency Synthesizer.

OR

Explain the role of PLL in the Frequency Shift Keying.

1. Explain the PIN Diagram of 555 Timer IC.

OR

Explain the 555 timer IC as an Astable Multivibrator.