

Department of Examination

Time Table of Back Improvement Exam, 2017

Back Improvement Exam – 2017 (05, 06 and 07 December 2017)

B. Tech VII Semester (Back)

Shift- I & II

Date: 20/11/2017

Day & Date Sem. Time Branch	TUESDAY (05/12/2017) VII 11:00am to 12:00pm	TUESDAY (05/12/2017) VII 2:00pm to 3:00pm	WEDNESDAY (06/12/2017) VII 11:00am to 12:00pm	WEDNESDAY (06/12/2017) VII 2:00pm to 3:00pm	THURSDAY (07/12/2017) VII 11:00am to 12:00pm	THURSDAY (07/12/2017) VII 2:00pm to 3:00pm
IT	DMWH	CG & MM	WCN	SPM	IP	ADBMS
CSE	Cloud Computing	ISS/CGMT	WCN (O)	ADMS	CC	VLSI (Code- 7464 & 7248)
ECE	AWP	DSP	WC	ICT / DIP	VLSI	VHDL
EEE	DBMS/PSP	PSA	PSE	UEPT / NCES	OS	AI
EE	PSP	PSA	PSE	NCES	OS/EOPS	AI
ME	CAD / FEM	RAC	OR	ST & SPP / OM	PDL / TURBO	CIM / NANO
Civil	WRE-I	DSS-I	DCS-II	TE-II	ANM	RWSS

[Signature]
Examinator in Charge

[Signature]
Principal